Manual Update Sheet

DATE: July 1, 2002

Document Being Updated: *TMS320F/C24x DSP Controllers Reference Guide: CPU and Instruction Set*

Literature Number Being Updated: SPRU160C

This Manual Update Sheet (literature number SPRZ019A) ships with the *TMS320F/C24x DSP Controllers Reference Guide: CPU and Instruction Set* (literature number SPRU160C).

Page: **Change or Add:**

7–151   **SAR (Store Auxiliary Register) Instruction**

Replace existing Opcode with the following:

Opcode

SAR ARx, *dma*

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	0	0	0		x		0				dma			

SAR ARx, *ind* [*,* **AR**n]

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	0	0	0		x		1		ARU		N		NAR	

Note: ARU, N, and NAR are defined in section 6.3, *Indirect Addressing Mode* (page 6–9).

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Mailing Address:

Texas Instruments
Post Office Box 655303
Dallas, Texas 75265